

FORM PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office		Attorney Docket No.: AHA-02101	Serial No.: 09/808,884	RECEIVED JUN 26 2001	
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use Several Sheets If Necessary)				Applicant: Eric John Hewitt et al.		Filing Date: March 14, 2001	
(37 CFR § 1.98(b))				Technology Center 2600			

U.S. PATENT DOCUMENTS							
Examiner Initials	Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date	
[Handwritten Initials]	AA	4,845,714	Zook	371	50	6/08/87	
	AB	5,406,570	Berrou et al.	371	43	4/16/92	
	AC	5,446,747	Berrou	371	45	4/16/92	
	AD	5,499,254	Ikekawa et al.	371	43	8/31/94	
	AE	5,563,897	Pyndiah et al.	371	37.4	11/18/94	
	AT	5,566,191	Ohnishi et al.	371	43	5/07/93	
	AF	5,684,811	Doran	371	43	9/01/95	
	AG	5,703,911	Lin et al.	375	341	8/17/95	
	AH	5,719,884	Roth et al.	371	37.4	7/27/95	
	AI	5,721,745	Hladik et al.	371	43	4/19/96	
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	AK	5,729,560	Hagenauer et al.	371	43.1	6/12/96	
	AL	5,761,248	Hagenauer et al.	375	340	7/19/96	
	AM	5,787,127	Ono et al.	375	341	7/22/96	
	AN	5,787,239	Horie et al.	395	114	7/17/96	
	AO	5,802,116	Baker et al.	375	341	4/04/96	
	AP	5,841,818	Lin et al.	375	341	1/17/96	
	AQ	5,901,182	Kot	375	341	3/26/97	
	AR	5,907,582	Yi	375	259	8/11/97	
	AS	5,930,272	Thesling	371	37.01	6/10/97	
AT	6,145,111	Crozier et al.	714	755	8/14/98		
AU							

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Examiner Initials	Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation		
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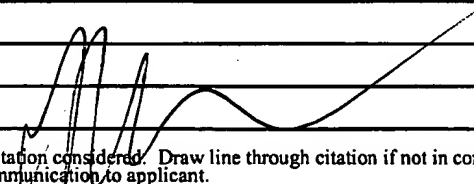
OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)	
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AB	A. Picart & R. Pyndiah, "Performance of Turbo-Decoded Product Codes Used in Multilevel Coding," IEEE, 1996, pp. 107-111.
AC	R. Pyndiah, "Near-Optimum Decoding of Product Codes: Block Turbo Codes," IEEE Transactions on Communications, Vol. 46, No. 8, Aug. 1998, pp. 1003-1010.

Examiner: [Signature]	Date Considered: 5/21/08
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EXAMINER:	Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
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	AD	J. Cheng & R. McEliece, "Frequency-Efficient Coding with Low-Density Generator Matrices," Aug. 1997 draft, (presented in part: October 1996 at 34th Allerton Conference on Communications, Control, and Computing, Monticello, Illinois; and 1997 IEEE International Symposium on Information Theory, Ulm, Germany, July 1997), pp. 1-30.			
	AE	S. Dolinar et al., "Code Performance as a Function of Block Size," TMO Progress Report 42-133, May 1998, pp. 1-23.			
	AF	D. Divsalar & F. Pollara, "Multiple Turbo Codes for Deep-Space Communications," TDA Progress Report 42-121, May 1995, pp. 66-77.			
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	AH	D. Divsalar & F. Pollara, "On the Design of Turbo Codes," TDA Progress Report 42-123, Nov. 1995, pp. 99-121.			
	AI	G. Battail et al., "Pseudo-Random Recursive Convolutional Coding For Near-Capacity Performance," IEEE, 1993, pp. 23-27.			
	AJ	M. Moher, "Decoding Via Cross-Entropy Minimization," IEEE, 1993, pp. 809-813.			
	AK	S. Benedetto et al., "Soft-Output Decoding Algorithms for Continuous Decoding of Parallel Concatenated Convolutional Codes," IEEE, 1996, pp. 112-117.			
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	AM	C. Berrou et al., "A Low Complexity Soft-Output Viterbi Decoder Architecture," IEEE, 1993, pp. 737-740.			
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	BA	L. Bahl et al., "Optimal Decoding of Linear Codes for Minimizing Symbol Error Rate," IEEE Transactions on Information Theory, Jan. 1972, pp. 284-287, International Symposium on Information Theory, Asilomar, CA.			
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	BC	S. Reddy & J. Robinson, "Random Error and Burst Correction by Iterated Codes," IEEE 1970, pp. 170-181.			
	BD	D. Chase, "A Class of Algorithms for Decoding Block Codes with Channel Measurement Information," IEEE Transactions on Information Theory Jan. 1972, pp. 170-181.			
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FORM PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office		Attorney Docket No.: AHA-02101		RECEIVED JUN 26 2001 Group Art Unit: 2621 Technology Center 2600	
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use Several Sheets If Necessary)				Applicant: Eric John Hewitt et al.		JUN 26 2001	
(37 CFR § 1.98(b))				Filing Date: March 14, 2001		JUN 26 2001	
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BH	L. Tolhuizen & C.P.M.G. Baggen, "On the weight enumerator of product codes," Discrete Mathematics, 1992, Vol. 106, No. 107 pp. 483-88.						
BI	F. Chiaraluce & R. Garelo, "On the Asymptotic Performance of Hamming Product Codes", ISCTA 01, pp. 1-6, July 15, 2001.						
BJ	H. Nickl et al., "Approaching Shannon's capacity limit by 0.27 dB using Hamming codes in a 'turbo'- decoding scheme," ISIT 1997.						
BK	J. Lodge et al., "Separable Map "Filters" For The Decoding Of Product and Concatenated Codes," IEEE, 1993, pp. 1740-1745.						
BL	J. Hagenauer & P. Hoeher, "A Viterbi Algorithm with Soft-Decision Outputs and its Applications," IEEE, 1989, pp. 1680-1686.						
BM	S. Hirasawa et al., "Modified Product Codes," IEEE 1984, Vol. IT-30, No. 2, pp. 299-306.						
BN	G. Ungerboeck, "Channel Coding with Multilevel/ Phase Signals," IEEE Transactions on Information Theory Vol. IT-28, No. 1, Jan. 1982.						
BO	S. Cambanis & M. Habib, "Finite Sampling Approximations for non-Band-Limited Signals," IEEE, Vol. 25, No. 5, July, 1981 p. 67.						
BP	U. Wachsmann et al., "Multilevel Codes: Theoretical Concepts and Practical Design Rules," IEEE, Vol. 25, No. 5, July 1999, pp. 1361-1391.						
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BU	C. Berrou, "Near Optimum Error Correcting Coding and Decoding: Turbo-Codes," IEEE, Vol. 44, No. 10, Oct. 1996, pp. 1261-1271.						
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Patent and Trademark Office

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Use Several Sheets If Necessary)

Applicant: Eric John Hewitt et al.

Filing Date: March 14, 2001

Group Art Unit: 2621

(37 CFR § 1.98(b))

U.S. PATENT DOCUMENTS

Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date
	AA	4,796,260	01/03/89	Schilling et al.	371	39	03/30/87
	AB	5,351,246	09/27/94	Blaum et al.	371	10.1	01/03/94
	AC	5,708,667	01/13/98	Hayashi	371	37.4	01/09/97
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AP	Eric Hewitt, "Turbo Product Codes for LMDS", 1999 IEEE, Advanced Hardware Architectures, Inc., pgs. 107-111.
AQ	Jian Wang et al., "Performance Bounds on Product Codes with Hyper-Diagonal Parity", XP-001034704, 3/15/00, pgs. WP7/1-6.
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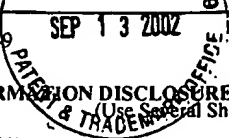
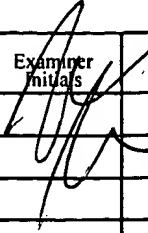


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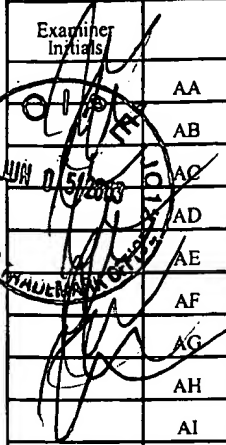
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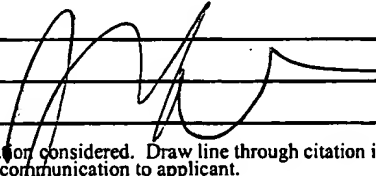
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Examiner Initials	Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date			
	AA	5,541,955	07/30/96	Jacobsmeier	375	222	04/28/95		
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	AQ	"Single-Bit Correct, Double-Bit Error Checking/Correction Scheme," IBM Technical Disclosure Bulletin, May 1994, No. NN9405101.							
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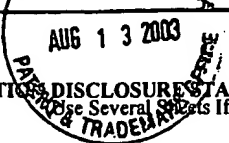
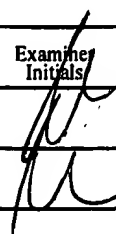
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	AI						
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OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)		
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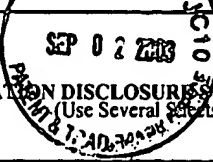
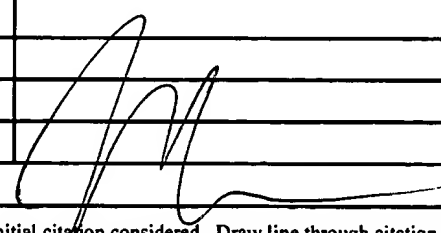
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	AB	2002/0101915 AI	08/01/02	Zhang et al.	375	222	07/23/01		
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	AB	5,657,354	08/12/97	Thesling, III et al.	375	332	05/15/95
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OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)	
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Applicants: Eric Hewitt et al.

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(37 CFR § 1.98(b))

U.S. PATENT DOCUMENTS

Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date
	AA	6,421,804 B1	07/16/02	Lee	714	755	12/20/99
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FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

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OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

	AP	
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